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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION OF

Park et al.

Group Art Unit: Unassigned

Appln. No.: 10/080,710

Examiner: Unassigned

Filed: February 25, 2002

Title: SYSTEM OF PERFORMING A REPAIR ANALYSIS FOR A SEMICONDUCTOR MEMORY DEVICE HAVING A REDUNDANT ARCHITECTURE

#4  
D. Scott  
4/29/03

January 24, 2003

\* \* \* \* \*

**INFORMATION DISCLOSURE STATEMENT**

I hereby certify that each enclosed document listed on the herewith Form PTO-1449 was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months ago by the attached Korean Office Action dated October 24, 2002. See that Office Action for information on each document.

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This IDS is intended to be in full compliance with the rules, but should the Examiner find any part of its required content to have been omitted, prompt notice to that effect is earnestly solicited, along with additional time under Rule 97(f), to enable Applicant to comply fully.

Contingent IDS Request Under Rule 97(d): Should a Final Rejection or Notice of Allowance issue with a mailing date which precedes or is the same as the filing date of this IDS, please consider this a Request under Rule 97(d), charge the IDS fee (Rule 17(p)) to our Deposit Account No. 03-3975 under Order No. 82124/284140, and proceed to consider this IDS under Rule 97(d).

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed Form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Respectfully submitted,  
PILLSBURY WINTHROP LLP



Glenn J. Perry  
Reg. No.: 28458

P.O. Box 10500  
McLean, VA 22102

(703) 905-2000